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CUSTOMER ACCEPTANCE SPECIFICATIONS MODEL NO. : <u>EIML123022DYA</u> (RoHS) FOR MESSRS : _____ CUSTOMER'S APPROVAL DATE : _____ BY : _____		

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SUMMARY

MAY.23, 2025

17

11.1 POWER SUPPLY FOR LCM

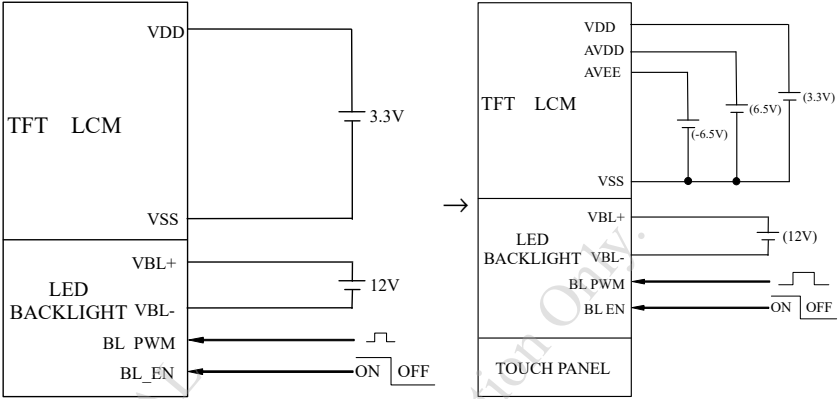


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1. GENERAL SPECIFICATIONS

1.1 DATA SHEETS FOR CONTROLLER / DRIVER PLEASE REFER TO :

HIMAX HX83192-C OR EQUIVALENT

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE), BIS(2-ETHYLHEXYL) PHTHALATE (DEHP), BUTYL BENZYL PHTHALATE (BBP), DIBUTYL PHTHALATE (DBP), DIISOBUTYL PHTHALATE (DIBP).

2. MECHANICAL SPECIFICATIONS

2.1 TFT LCD MODULE MECHANICAL SPECIFICATIONS

(1) DISPLAY SIZE	-----	12.3 inch
(2) NUMBER OF DOTS	-----	1920(RGB)W * 720H DOTS
(3) MODULE SIZE	-----	318.03W * 138.51H * 14.2D(MAX.) mm (WITHOUT FPC)
(4) ACTIVE AREA	-----	292.032W * 109.512H mm
(5) DOT SIZE	-----	0.0507W * 0.1521H mm
(6) PIXEL SIZE	-----	0.1521W * 0.1521H mm
(7) LCD TYPE	-----	TFT, IPS, TRANSMISSIVE, NORMALLY BALCK
(8) COLOR	-----	16.7M
(9) VIEWING DIRECTION	-----	SUPER WIDE VIEW
(10) BACKLIGHT	-----	LED , COLOR : WHITE
(11) INTERFACE MODE	-----	LVDS(VESA)
(12) WEIGHT	-----	TBD

2.2 CAPACITIVE TOUCH PANEL MECHANICAL SPECIFICATIONS

- (1) TOUCH PANEL SIZE ----- 12.3 inch
- (2) OUTER DIMENSION ----- 318.03W * 138.51H mm
(WITHOUT FPC)
- (3) ACTIVE AREA ----- 292.032W * 109.512H mm
- (4) INPUT TYPE ----- MULTI TOUCH
- (5) NUMBER OF TOUCH SENSOR ----- 56*21 SENSORS
- (6) RESOULTION ----- 1920 * 720
- (7) INTERFACE MODE ----- I2C
- (8) THE MATERIAL OF COVER LENS ----- GLASS

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	3.6	V	—
	AVDD-VSS	-0.3	7	V	—
	AVEE-VSS	-7.3	0.3	V	—
STATIC ELECTRICITY	—	—	—	V	NOTE (1)

NOTE (1) : LCM SHOULD BE GROUND DURING LCM HANDLING.

NOTE (2) : THE ABSOLUTE MAXIMUM RATING VALUES OF THIS PRODUCT ARE NOT ALLOWED TO BE EXCEEDED AT ANY TIMES. SHOULD A MODULE BE USED WITH ANY OF THE ABSOLUTE MAXIMUM RATINGS EXCEEDED, THE CHARACTERISTICS OF THE MODULE MAY NOT BE RECOVERED, OR IN AN EXTREME CASE, THE MODULE MAY BE PERMANENTLY DESTROYED.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	(-20°C)	(70°C)	(-30°C)	(80°C)	NOTE (1),(2),(3)
HUMIDITY	NOTE (3)		NOTE (3)		WITHOUT CONDENSATION
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		

NOTE (1) : THE ABSOLUTE MAXIMUM RATINGS OF THIS PRODUCT SHOULD NOT BE EXCEEDED AT ANY TIME. IF THESE RATINGS ARE EXCEEDED, THE PRODUCT'S PERFORMANCE IS NOT GUARANTEED AND THE PRODUCT MAY EXPERIENCE PERMANENT DAMAGE.

NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3) : Ta ≤ 60°C : 90%RH MAX.

Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C.

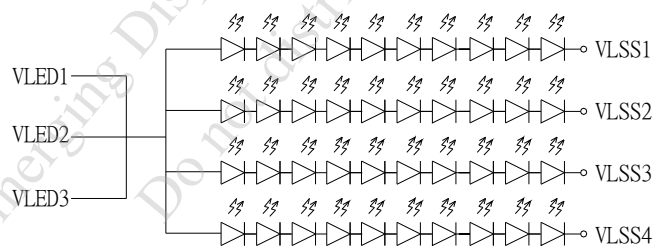
4. ELECTRICAL CHARACTERISTICS

Ta = 25 °C

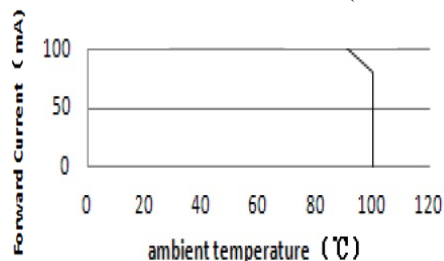
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	—	(3.0)	(3.3)	(3.6)	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VBL+-VBL-	—	—	(12)	—	V	
POWER SUPPLY VOLTAGE FOR CIRCUITS	AVDD-VSS	—	(6.0)	(6.5)	(7.0)	V	
	AVEE-VSS	—	(-7.0)	(-6.5)	(-6.0)	V	
INPUT HIGH LEVEL VOLATGE	VIH	ALL INPUT PAD WITH VDD	0.7VDD	—	VDD	V	
INPUT LOW LEVEL VOLTAGE	VIL		0	—	0.3VDD	V	
POWER SUPPLY CURRENT	IDD	VDD-VSS=(3.30)V	—	(116)	TBD	mA	NOTE (1)
POWER SUPPLY CURRENT FOR CIRCUITS	IAVDD	AVDD-VSS=(6.5)V	—	(71)	TBD		
	IAVEE	AVEE-VSS=(-6.5)V	—	(76)	TBD		
POWER SUPPLY CURRENT FOR LED DRIVER	IBL+	VBL+-VBL- =12V LED B/L=ON PWM=100%	—	(1200)	—	mA	
POWER SUPPLY VOLTAGE FOR LED DRIVER	BL_EN	VIH	(1.6)	—	—	V	
		VIL	—	—	(0.8)	V	
	BL_PWM	VIH	(1.6)	—	—	V	
		VIL	—	—	(0.8)	V	
LED LIFE TIME	—	(IF=100mA) (PER LED)	(50K)	—	—	HRS	NOTE (4) NOTE (5)

NOTE (1) : THE DISPLAY PATTERN IS ALL “WHITE”.

NOTE (2) : INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (3) : AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)



NOTE (4) : CONDITIONS; Ta=25 °C, CONTINUOUS LIGHTING.

NOTE (5) : DEFINITIONS OF LIFE TIME

LCD LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

5. TIMING CHARACTERISTICS

5.1 SIGNAL ELECTRICAL CHARACTERISTICS

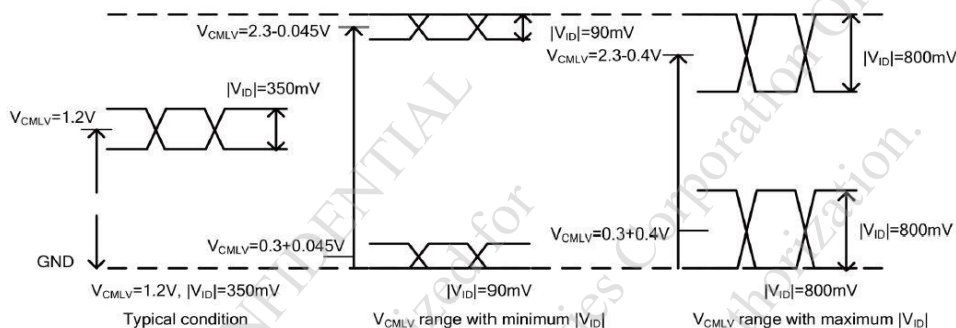
5.1.1 SIGNAL ELECTRICAL CHARACTERISTICS FOR LVDS RECEIVER

ITEM	SYMBOL	SPEC.			
		MIN.	TYP.	MAX.	UNIT
POSITIVE-GOING INPUT THRESHOLD VOLTAGE	V_{TH}	—	—	90	mV
NEGATIVE-GOING INPUT THRESHOLD VOLTAGE	V_{TL}	-90	—	—	mV
LVDS DIFFERENTIAL VOLTAGE	$ V_{ID} $	90	350	800	mV
LVDS INPUT COMMON MODE VOLTAGE	V_{CMLV}	0.3+ $ V_{ID} /2$	—	2.3- $ V_{ID} /2$	V

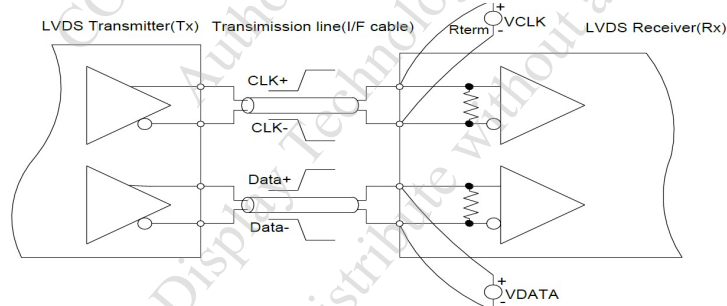
NOTE:(1)REQUIREMENT OF TERMINATION RESISTANCE(Ω) : 80(MIN.)/100(TYP.)/120(MAX).

(2)TEST CONDITIONS: V_{CMLV} =1.2V, V_{DD1} =2.7V~3.6V.

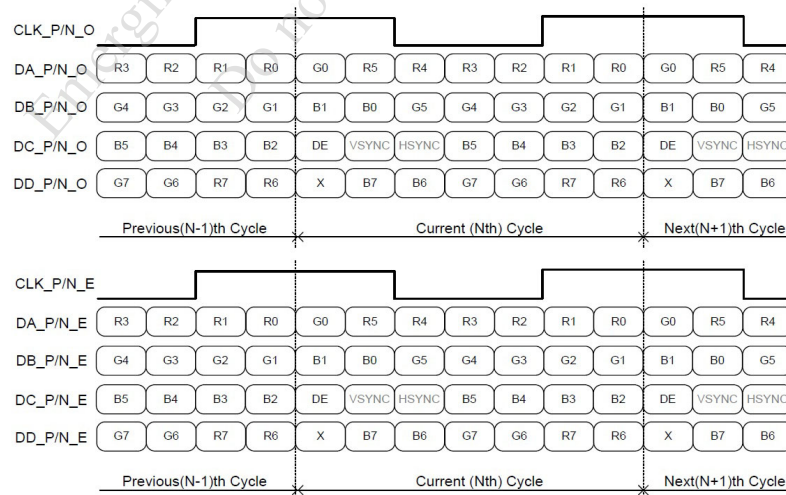
(3)TEST CONDITION: TEST POINT IS IC PAD.



LVDS RECEIVER INPUT SIGNAL LEVELS

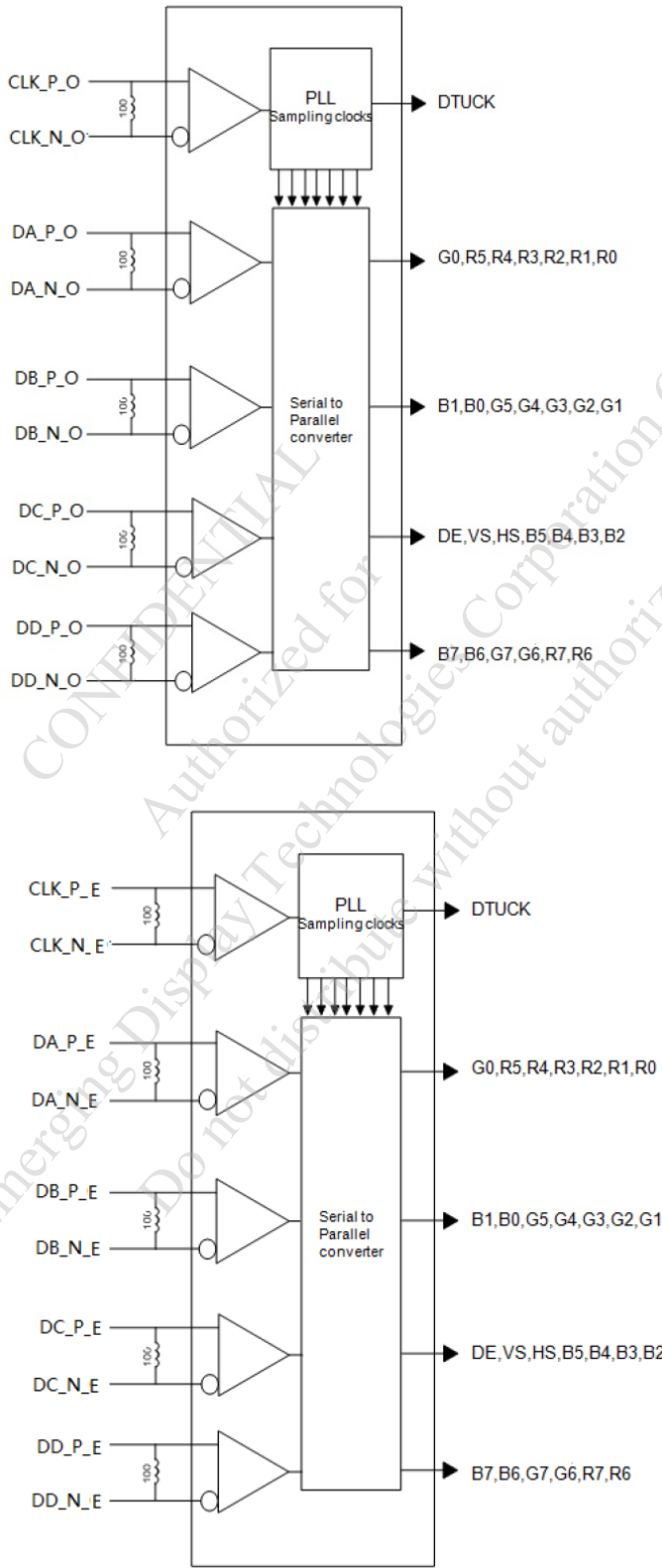


MEASUREMENT SYSTEM



DATA MAPPING

5.1.2 LVDS RECEIVER INTERNAL CIRCUIT



5.1.3 LVDS INTERFACE TIMING

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
H TOTAL TIME	HT	1992	2038	2112	CLOCKS
H ACTIVE TIME	HA	1920			CLOCKS
H FRONT PORCH	THFP	20	56	110	CLOCKS
H SYNC PULSE WIDTH	HSPW	32	32	32	CLOCKS
H BACK PORCH	THBP	20	30	50	CLOCKS
V TOTAL TIME	VT	850	850	850	LINES
V ACTIVE TIME	VA	720			LINES
V FRONT PORCH	TVFP	120	120	120	LINES
V SYNC PULSE WIDTH	VSPW	2	2	2	LINES
V BACK PORCH	TVBP	8	8	8	LINES
FRAME RATE	VSNC	60			Hz
PIXEL CLOCK FREQUENCY	FCLK	(101.59)	(103.94)	(107.71)	MHz

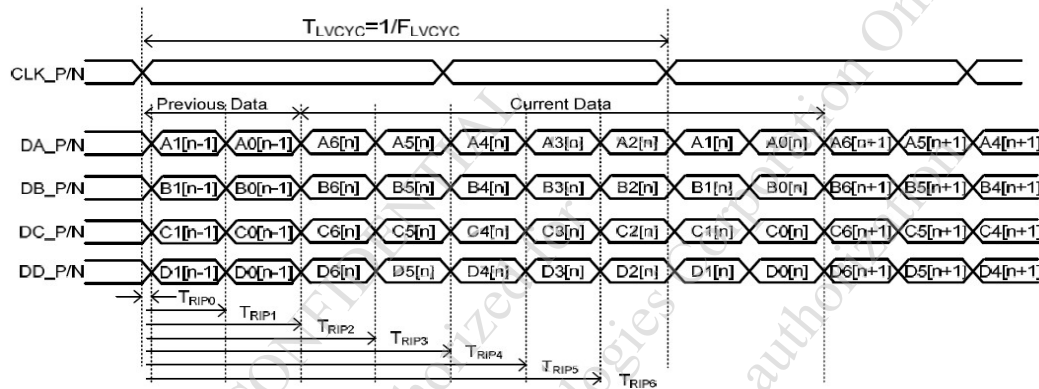
NOTE(1): $HT \cdot VT \cdot FV < 107.71 \text{ MHz}$

NOTE(2): ALL RELIABILITIES ARE SPECIFIED FOR TIMING SPECIFICATION BASED ON REFRESH RATE OF 60HZ. A1233H R2-P IS SECURED ONLY FOR FUNCTION UNDER LOWER REFRESH RATE;

5.2 AC CHARACTERISTICS

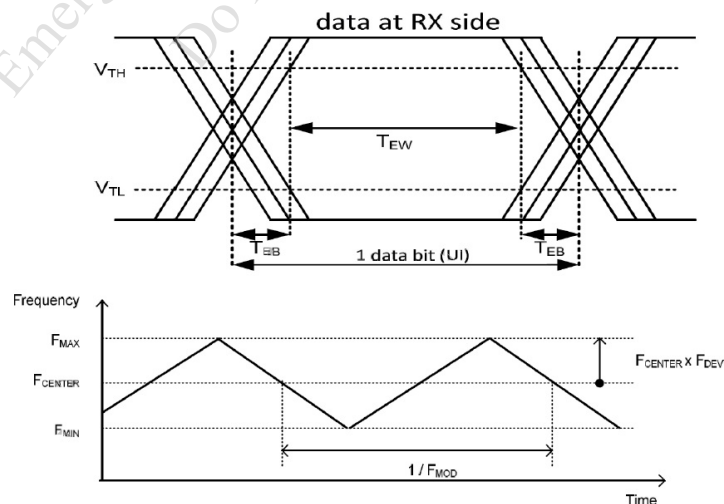
5.2.1 AC CHARACTERISTICS-1

ITEM	SYMBOL	SPEC.			UNIT
		MIN.	TYP.	MAX.	
CLOCK FREQUENCY	F_{LVCYC}	—	—	140	MHZ
CLOCK PERIOD	T_{LVCYC}	—	—	140	MHZ
1 DATA BIT TIME	UI	7.14	—	—	ns
INPUT DATA POSITION FOR BIT0	T_{RIP0}	—	1/7	—	T_{LVCYC}
INPUT DATA POSITION FOR BIT1	T_{RIP1}	$-T_{EB}$	0	$+T_{EB}$	UI
INPUT DATA POSITION FOR BIT2	T_{RIP2}	$1-T_{EB}$	1	$1+T_{EB}$	UI
INPUT DATA POSITION FOR BIT3	T_{RIP3}	$2-T_{EB}$	2	$2+T_{EB}$	UI
INPUT DATA POSITION FOR BIT4	T_{RIP4}	$3-T_{EB}$	3	$3+T_{EB}$	UI
INPUT DATA POSITION FOR BIT5	T_{RIP5}	$4-T_{EB}$	4	$4+T_{EB}$	UI
INPUT DATA POSITION FOR BIT6	T_{RIP6}	$5-T_{EB}$	5	$5+T_{EB}$	UI

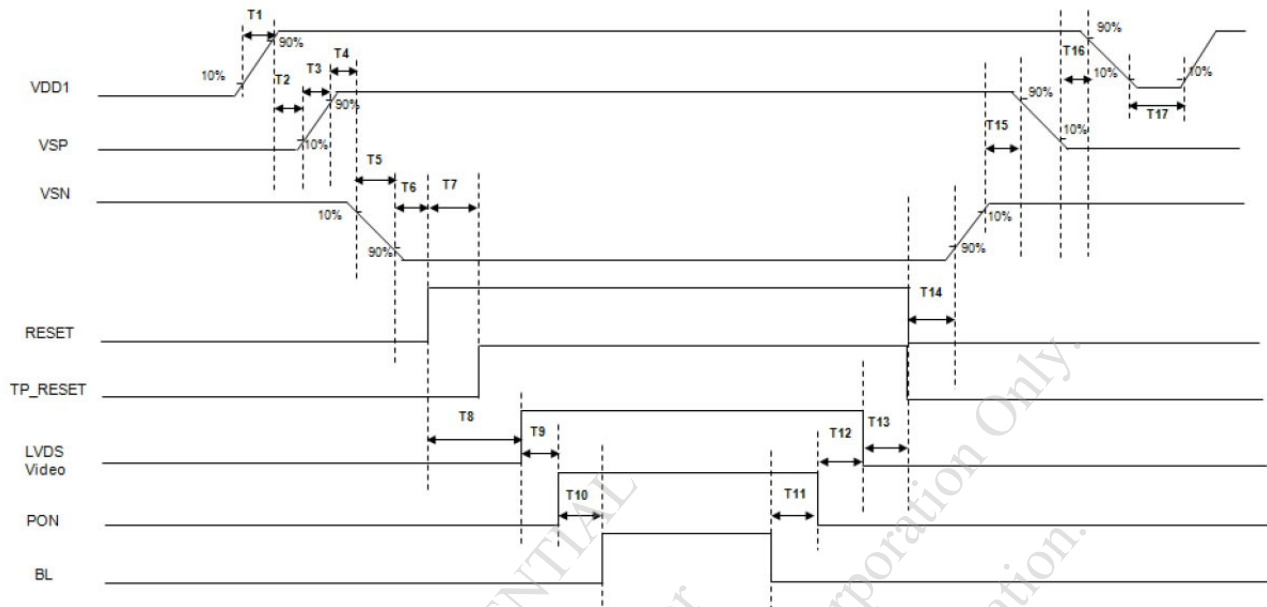


5.2.2 AC CHARACTERISTICS-2

ITEM	SYMBOL	SPEC.			UNIT
		MIN.	TYP.	MAX.	
INPUT EYE BORDER(LVDS < 100MHZ)	T_{EB}	—	—	0.25	UI
INPUT EYE BORDER(LVDS ≥ 100MHZ)	T_{EB}	—	—	0.2	UI
INPUT EYE WIDTH(LVDS < 100MHZ)	T_{EW}	0.5	—	—	UI
INPUT EYE WIDTH(LVDS ≥ 100MHZ)	T_{EW}	0.6	—	—	UI
MAXIMUM DEVIATION OF INPUT CLOCK FREQUENCY DURING SSC	F_{DEW}	—	—	$\pm 3^{(1)}$	%
MAX./MIN. MODULATION FREQUENCY OF INPUT CLOCK DURING SSC	F_{MOD}	15	—	200	KHZ



5.3 POWER ON/OFF SEQUENCE



POWER SEQUENCING REQUIREMENTS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
VDD1 RISE TIME	T1	0.1	—	5	ms
VDD1 TO VSP	T2	1	—	—	ms
VSP RISE TIME	T3	0.1	—	10	ms
VSP TO VSN	T4	0	—	—	ms
VSN RISE TIME	T5	0.1	—	10	ms
VSN VALID TO RESET VALID	T6	5	—	—	ms
RESET VALID TO TP RESET VALID	T7	0	—	—	ms
RESET VALID TO SIGNAL VALID	T8	0	—	—	ms
SIGNAL VALID TO PON VALID	T9	16	—	—	ms
PON VALID TO BACKLIGHT ON	T10	200	—	—	ms
BACKLIGHT OFF TO PON DISABLE	T11	200	—	—	ms
PON DISABLE TO SIGNAL DISABLE	T12	6	—	—	FRAME
SIGNAL DISABLE TO RESET DISABLE	T13	0	—	—	ms
RESET DISABLE TO VSN DOWN	T14	0	-	-	ms
VSN DOWN TO VSP DOWN	T15	0	-	-	ms
VSP DOWN TO VDD1 DOWN	T16	0	-	-	ms
VDD1 NORMAL POWER OFF TO NORMAL POWER ON	T17	100	-	-	ms

6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS (NOTE 1)

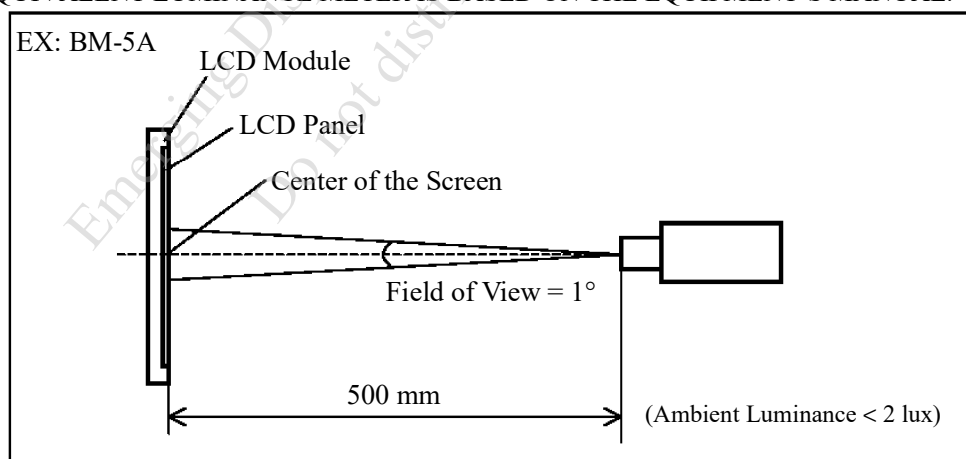
Ta=25±2°C

ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT	REMARK
VIEWING ANGLE		θ_{y+}	$CR \geq 10$	$\theta_x=0^\circ$	(80)	(85)	—	deg.	NOTE (2) NOTE (3)
		θ_{y-}			(80)	(85)	—		
		θ_{x+}		$\theta_y=0^\circ$	(80)	(85)	—		
		θ_{x-}			(80)	(85)	—		
CONTRAST RATIO(CENTER)		CR	$\theta_x=0^\circ$, $\theta_y=0^\circ$		(1100)	—	—	—	NOTE (3)
RESPONSE TIME		Tr + Tf			—	25	30	msec	NOTE (4)
COLOR CHROMATICITY (CENTER)	WHITE	Wx	LED B/L=ON PWM=100% VBL+-VBL=-(12V)		(0.25)	(0.30)	(0.35)	—	NOTE (5)
		Wy			(0.28)	(0.33)	(0.38)		
	RED	Rx			(0.59)	(0.64)	(0.69)	—	
		RY			(0.27)	(0.32)	(0.37)		
	GREEN	Gx			(0.23)	(0.28)	(0.33)	—	
		Gy			(0.55)	(0.60)	(0.65)		
	BLUE	Bx			(0.09)	(0.14)	(0.19)	—	
		By			(0.05)	(0.10)	(0.15)		
THE BRIGHTNESS OF MODULE (CENTER)		B			(900)	(1000)	—	cd/m ²	NOTE (6)
THE UNIFORMITY OF MODULE		—			(70)	—	—	%	NOTE (7)

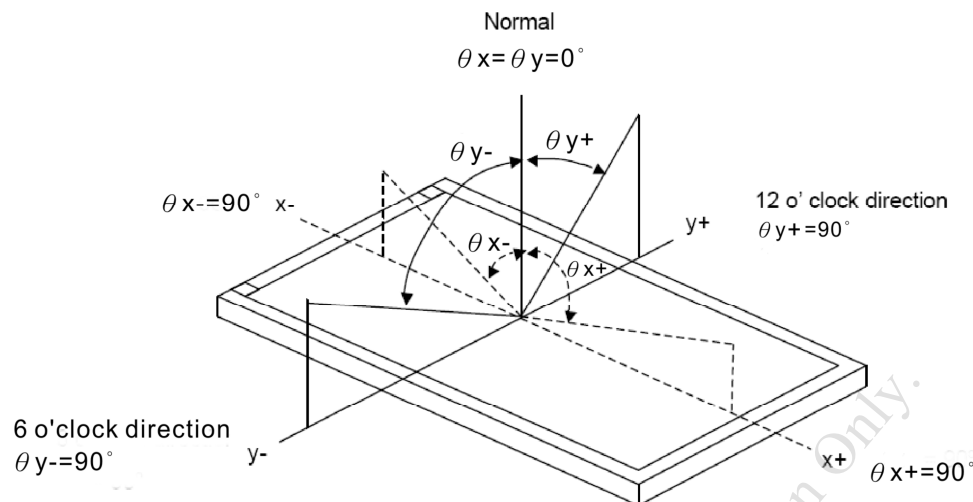
NOTE (1) : TEST CONDITION :

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.

THE MEASUREMENT CRITERIA AND CONDITION FOR EITHER BM-5A OR CA-210 OR EQUIVALENT LUMINANCE METER IS BASED ON THE EQUIPMENT'S MANUAL.



NOTE (2) : DEFINITION OF VIEWING ANGLE :



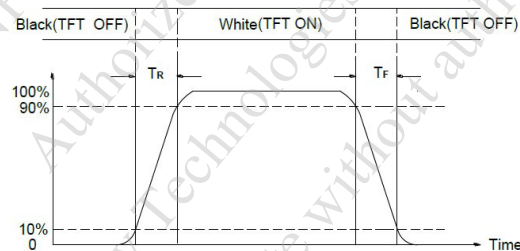
NOTE (3) : DEFINITION OF CONTRAST RATIO (CR) :

MEASURED AT THE CENTER POINT OF MODULE

$$\text{CONTRAST RATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$$

NOTE (4) : DEFINITION OF RESPONSE TIME : T_R AND T_F

THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



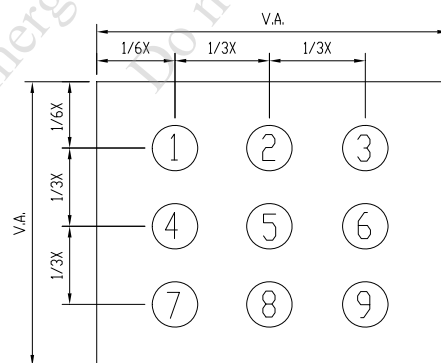
NOTE (5) : DEFINITION OF COLOR CHROMATICITY

(a)100% RGB PIXEL DATA TRANSMISSION WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY POWERED ON.

(b)MEASURED AT THE CENTER POINT OF MODULE

NOTE (6) : MEASURED THE BRIGHTNESS OF WHITE STATE AT CENTER POINT.

NOTE (7) : (a)DEFINITION OF BRIGHTNESS UNIFORMITY

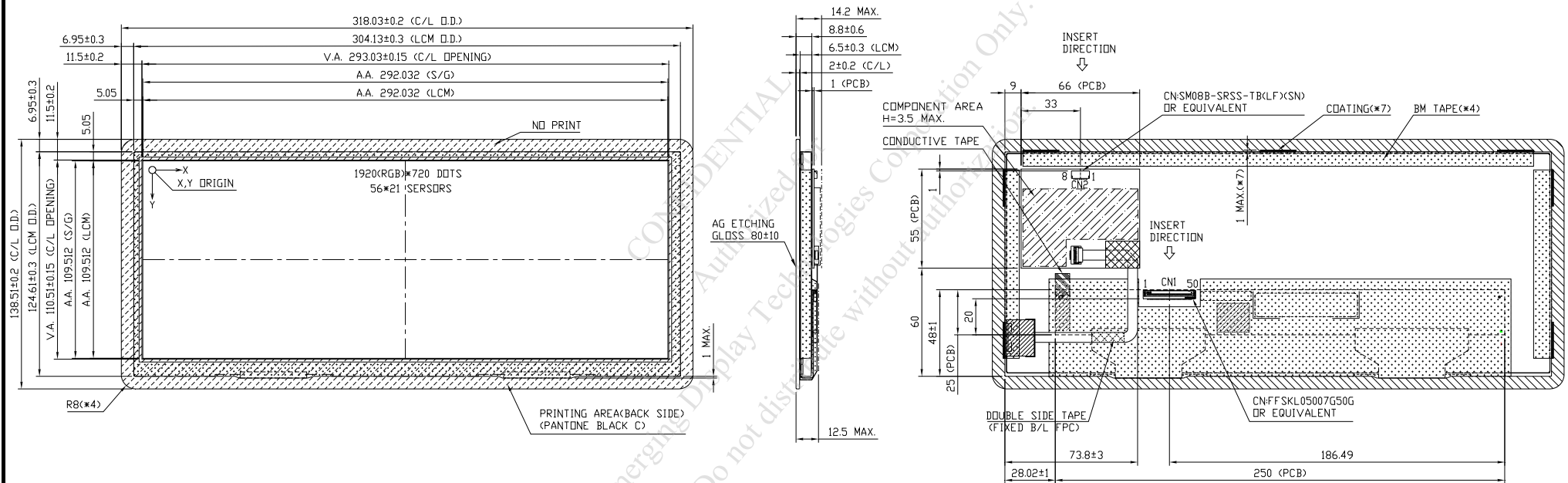


UNIT : mm

(b)THE BRIGHTNESS UNIFORMITY CALCULATING METHOD

$$\text{UNIFORMITY} = \frac{\text{MINIMUM BRIGHTNESS}}{\text{MAXIMUM BRIGHTNESS}} * 100\%$$

7. OUTLINE DIMENSIONS



UNIT : mm

SCALE : NTS

THIRD ANGLE PROJECTION

NOT SPECIFIED TOLERANCE IS ± 0.5 mm

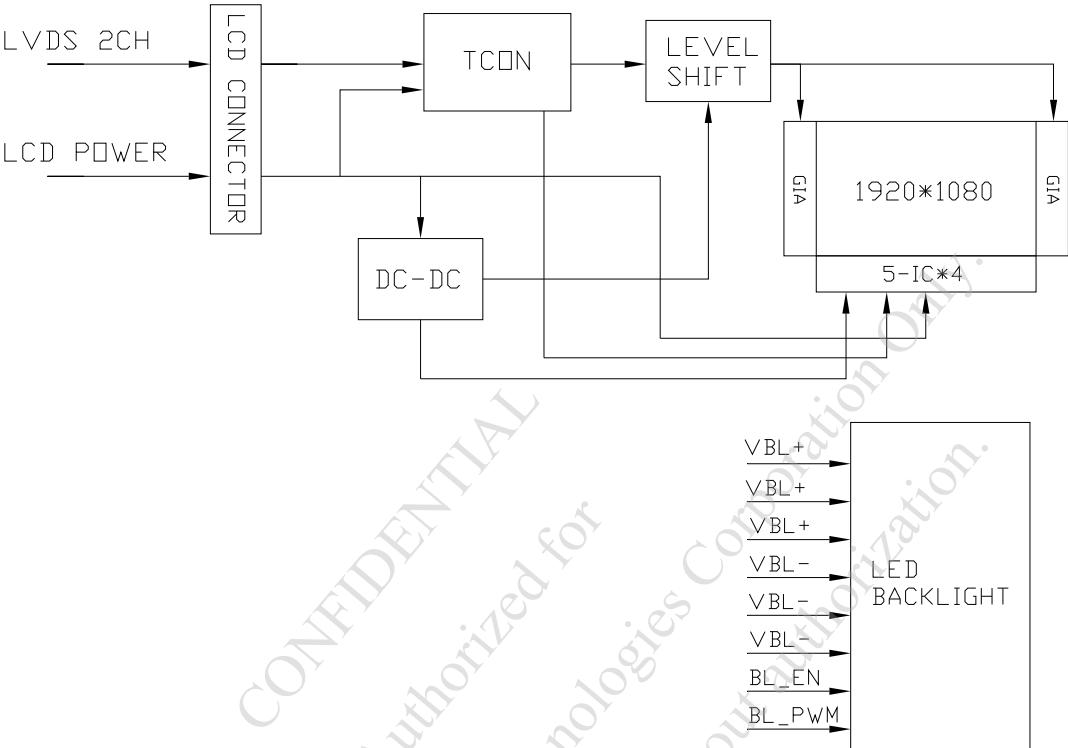
NOTE:

1. C/L GLASS : SODA-LIME, CHAMFERED EDGES.

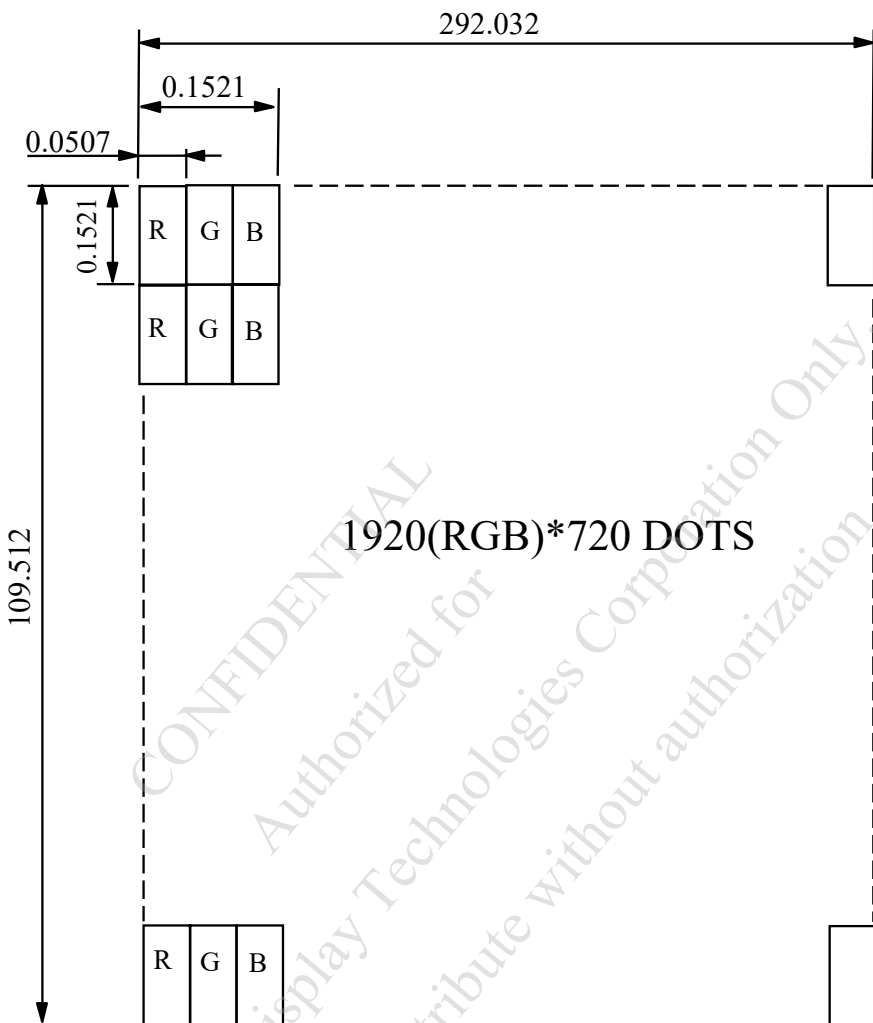
2. SUPPORTS ALL ANGLE VIEWABILITY WITH POLARIZED SUNGLASSES.

8. BLOCK DIAGRAM

8.1 TFT AND CTP MODULE



9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm
SCALE : NTS
NOT SPECIFIED TOLERANCE IS ± 0.1
DOTS MATRIX TOLERANCE IS ± 0.01

10. INTERFACE SIGNALS

IF1

PIN NO.	SYMBOL	FUNCTION
1	NC	EDT INTERNAL USE ONLY(EXT POWER SUPPLY FOR OTP CIRCUIT 7.25V)
2	VSS	DIGITAL GROUND
3	DA P O	LVDS INPUT
4	DA N O	LVDS INPUT
5	VSS	DIGITAL GROUND
6	DB P O	LVDS INPUT
7	DB N O	LVDS INPUT
8	VSS	DIGITAL GROUND
9	DC P O	LVDS INPUT
10	DC N O	LVDS INPUT
11	VSS	DIGITAL GROUND
12	CLK P O	LVDS INPUT
13	CLK N O	LVDS INPUT
14	VSS	DIGITAL GROUND
15	DD P O	LVDS INPUT
16	DD N O	LVDS INPUT
17	VSS	DIGITAL GROUND
18	DA P E	LVDS INPUT
19	DA N E	LVDS INPUT
20	VSS	DIGITAL GROUND
21	DB P E	LVDS INPUT
22	DB N E	LVDS INPUT
23	VSS	DIGITAL GROUND
24	DC P E	LVDS INPUT
25	DC N E	LVDS INPUT
26	VSS	DIGITAL GROUND
27	CLK P E	LVDS INPUT
28	CLK N E	LVDS INPUT
29	VSS	DIGITAL GROUND
30	DD P E	LVDS INPUT
31	DD N E	LVDS INPUT
32	VSS	DIGITAL GROUND
33	VDD	DIGITAL POWER,3.3V
34	VDD	DIGITAL POWER,3.3V
35	AVDD	DIGITAL POWER,6.5V
36	NC	NC
37	AVEE	DIGITAL POWER,-6.5V
38	VSS	DIGITAL GROUND
39	TP_I2C_SDA	I2C INTERFACE DATA SIGNAL FOR TOUCH
40	TP_I2C_SCL	I2C INTERFACE CLOCK SIGNAL FOR TOUCH
41	TP_I2C_INT	TOUCH SCREEN INTERRUPT LINE
42	TP_EXT_RSTN	TP EXTERNAL RESET SIGNAL
43	BIST_EN	ENABLE BIST FUNCTION
44	TP_GPIO[0]	USED FOR LOW POWER WAKE UP GESTURE FUNCTION
45	PON	SLPIN/SLPOUT HARDWARE CONTROL SIGNAL
46	TP_GPIO[1]	OUTPUT TOUCH FAIL DETECTION
47	RESX	RESET PIN , D-IC IS INITIALIZED WHEN THIS PIN IS LOW
48	DD_SDI_SDA	I2C INTERFACE DATA SIGNAL FOR OTP
49	DD_SCL	I2C INTERFACE CLOCK SIGNAL FOR OTP
50	FAIL_DET	FAIL DETECTION SIGNAL OUTPUT

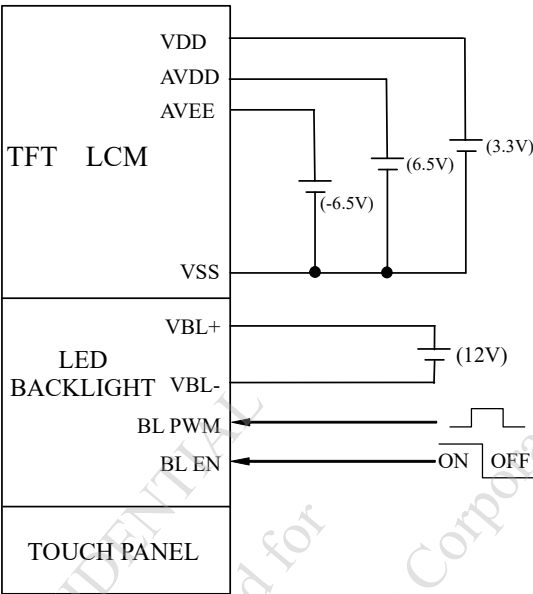
CN2

PIN NO.	SYMBOL	FUNCTION
1	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
2	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
3	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
4	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
5	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
6	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
7	BL_EN	BACKLIGHT LED ON/OFF CONTROL
8	BL_PWM	BACKLIGHT LED BRIGHTNESS CONTROL

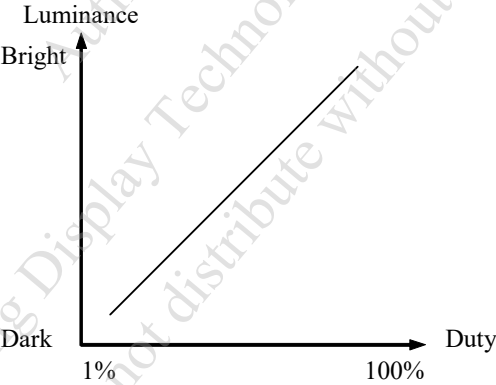
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11. POWER SUPPLY

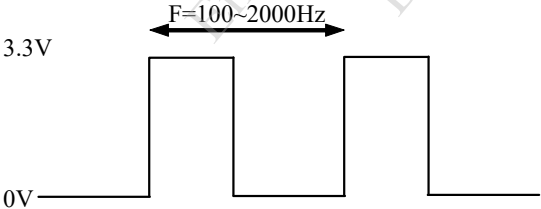
11.1 POWER SUPPLY FOR LCM



NOTE (1) : ADJUST THE PWM SIGNAL IN ORDER TO CONTROL LED BACKLIGHT'S BRIGHTNESS. THE HIGHER THE DUTY CYCLE, THE HIGHER THE BRIGHTNESS



NOTE (2) : PWM SIGNAL OPERATION FREQUENCY IS 100~2000 Hz AND DIMMING DUTY.



PWM Dimming Frequency[Hz]	Dimming Duty	
	Min[%]	Max[%]
100 < F _{DIM} < 2K	1	100

12. CAPACITIVE TOUCH PANEL SPECIFICATION

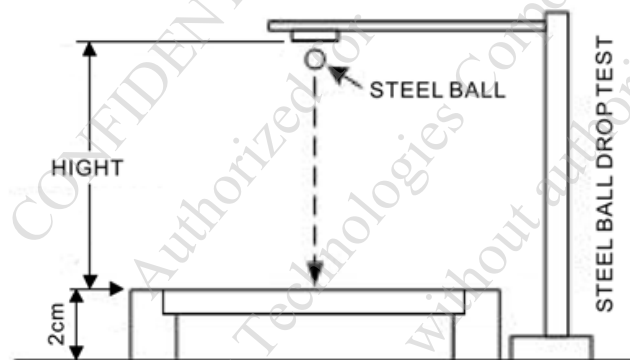
12.1 HARDNESS

ITEM	DESCRIPTION
SURFACE HARDNESS	(7)H (MIN.)

12.2 DURABILITY

USING STEEL BALL AND FALLING ON TOUCH PANEL SURFACE, FROM THE HEIGHT MUST PASS BELOW CONDITIONS :

ITEM	CONDITION	INSPECTION METHOD	DESCRIPTION
STEEL BALL DROP TEST	WEIGHT : 67g HEIGHT OF FALL : 30 cm	VISUAL INSPECTION	SIGN OF FRACTURE OR DAMAGE IS NOT ACCEPTABLE 3 TIME/ 1 POINTS, 25°C (CENTER POINT)



13 INSPECTION CRITERIA

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

13.2 INSPECTION CONDITIONS

13.2.1 (1)OBSERVATION DISTANCE : $35\pm 5\text{cm}$

(2)VIEWING ANGLE : $\pm 45^\circ$

$\pm 45^\circ$ (FOR SECTION WITHIN VIEWING AREA), REFER TO FIG.A
 90° (FOR SECTION OUTSIDE OF VIEWING AREA), REF TO FIG.B
 PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN 45°

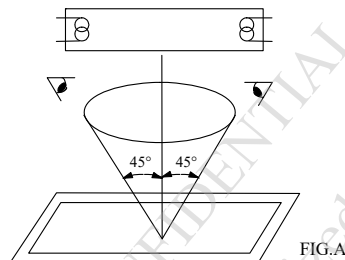


FIG.A

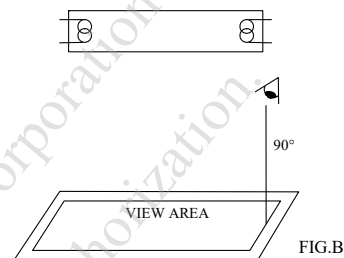


FIG.B

THE INSPECTION CRITERIA IS ACCORDING TO LINE OF SIGHT. INSPECTION SHALL BE MADE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT OF 45° WITH RESPECT TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE FLUORESCENT LAMP AND THE CONE AXIS MUST BE PERPENDICULAR TO THE LCD SURFACE.

IF THE DEFECTS ARE OUTSIDE OF VIEWING AREA, IT SHALL BE INSPECTED BY 90° WITH RESPECT TO THE VERTICAL AXIS FROM EDGE OF VIEWING AREA.

13.2.2 ENVIRONMENT CONDITIONS :

AMBIENT TEMPERATURE		$20\pm 5^\circ\text{C}$
AMBIENT HUMIDITY		$55 \pm 20\%\text{RH}$
AMBIENT ILLUMINATION	COSMETIC INSPECTION	1000~1200lux
	FUNCTIONAL INSPECTION	100~150 lux
INSPECTION TIME		15 secs

13.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD :

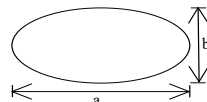
ANSI/ ASQ Z1.4 NORMAL INSPECTION LEVEL II

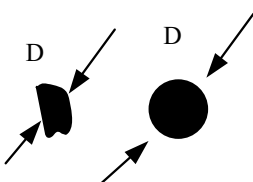
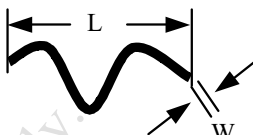
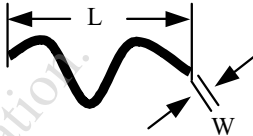
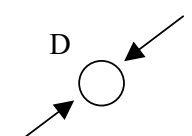
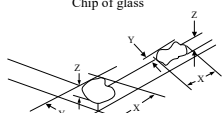
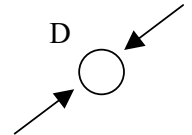
(b)AQL : MAJOR DEFECT : AQL 0.65

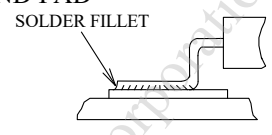
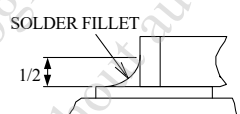
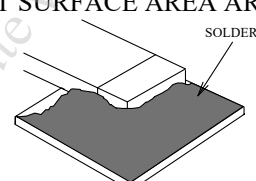
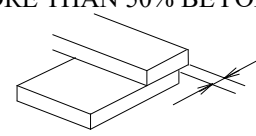
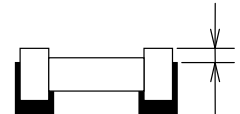
MINOR DEFECT : AQL 1.0

13.3 DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS - EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
	2.CTP FUNCTION	• NO FUNCTION • BROKEN LINE • FALSE TOUCH	
	3.BACKLIGHT	• NO LIGHT • FLICKERING AND OTHER ABNORMAL ILLUMINATION	
	4.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
MINOR DEFECT	1.DISPLAY ZONE	• BLACK/WHITE SPOT • BUBBLES ON POLARIZER • NEWTON RING • BLACK/WHITE LINE • SCRATCH • CONTAMINATION • UNEVEN COLOR SPREAD	1.0
	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	
	3.SOLDERING	• INSUFFICIENT SOLDER • SOLDERED IN INCORRECT POSITION • CONVEX SOLDERING SPOT • SOLDER BALLS • SOLDER SCRAPS	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

NO.	ITEM	CRITERIA																																										
1	DISPLAY ON INSPECTION	INCORRECT PATTERN MISSING SEGMENT DIM SEGMENT OPERATING VOLTAGE BEYOND SPEC																																										
2	OVERALL DIMENSIONS	OVERALL DIMENSION BEYOND SPEC																																										
3	DOT DEFECT	INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS. <table border="1"> <thead> <tr> <th colspan="2">ITEM</th><th>STANDARD</th></tr> <tr> <th colspan="2"></th><th>Z</th></tr> </thead> <tbody> <tr> <td rowspan="3">BRIGHT DOT</td><td>RANDOM</td><td>N≤2</td></tr> <tr> <td>2 DOTS ADJACENT (PAIR)</td><td>N=0</td></tr> <tr> <td>3 DOTS ADJACENT OR MORE</td><td>N=0</td></tr> <tr> <td rowspan="3">DARK DOT</td><td>RANDOM</td><td>N≤4</td></tr> <tr> <td>2 DOTS ADJACENT (PAIR)</td><td>N=0</td></tr> <tr> <td>3 DOTS ADJACENT OR MORE</td><td>N=0</td></tr> <tr> <td rowspan="2">DISTANCE</td><td>MINIMUM DISTANCE BETWEEN BRIGHT DOTS</td><td>—</td></tr> <tr> <td>MINIMUM DISTANCE BETWEEN DARK DOTS</td><td>L≥10mm</td></tr> <tr> <td colspan="2">TOTAL BRIGHT AND DARK DOT</td><td>N≤4</td></tr> <tr> <td rowspan="2">SMALL BRIGHT DOT</td><td>DOT GATHERING</td><td>N≤3 DISTANCE≤20mm</td></tr> <tr> <td>DOT SCATTERING</td><td>N≤4</td></tr> <tr> <td colspan="2">MICRO BRIGHT DOT(ND 8% NOT OBSERVED)</td><td>N=0</td></tr> <tr> <td colspan="2">DISPLAY FAILURE (V-LINE/H-LINE/CROSS LINE ETC.)</td><td>NOT ALLOWABLE</td></tr> <tr> <td>MURA</td><td>JUDGE BY LIMIT SAMPLE OR NOT VISIBLE THROUGH ND FILTER</td><td>ND 5%</td></tr> </tbody> </table> NOTE : (1)THE DEFINITION OF DOT: DOTS THAT CAN BE SEEN THROUGH A 8% ND FILTER,AND THE SIZE OF A DEFECTIVE DOT OVER IS 1/2 OF WHOLE DOT. (2)BRIGHT DOT: DOTS THAT CAN BE SEEN THROUGH A 8% ND FILTER, AND THE SIZE OF A DEFECTIVE DOT IS THE WHOLE DOT. (3)DARK DOT: DOTS APPEAR DARK AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER PURE RED, GREEN, BLUE PICTURE. (4)MICRO BRIGHT DOT: DOTS THAT CANNOT BE SEEN THROUGH A 8% ND FILTER, AND THE SIZE OF A DEFECTIVE DOT IS THE WHOLE DOT. (5)SMALL BRIGHT DOT: THE SIZE OF A DEFECTIVE DOT IS LESS THAN 1/2 OF WHOLE DOT. (6)2 DOT ADJACENT=1PAIR=2DOTS.  2 dot adjacent  2 dot adjacent  2 dot adjacent(vertical)  2 dot adjacent(slant)	ITEM		STANDARD			Z	BRIGHT DOT	RANDOM	N≤2	2 DOTS ADJACENT (PAIR)	N=0	3 DOTS ADJACENT OR MORE	N=0	DARK DOT	RANDOM	N≤4	2 DOTS ADJACENT (PAIR)	N=0	3 DOTS ADJACENT OR MORE	N=0	DISTANCE	MINIMUM DISTANCE BETWEEN BRIGHT DOTS	—	MINIMUM DISTANCE BETWEEN DARK DOTS	L≥10mm	TOTAL BRIGHT AND DARK DOT		N≤4	SMALL BRIGHT DOT	DOT GATHERING	N≤3 DISTANCE≤20mm	DOT SCATTERING	N≤4	MICRO BRIGHT DOT(ND 8% NOT OBSERVED)		N=0	DISPLAY FAILURE (V-LINE/H-LINE/CROSS LINE ETC.)		NOT ALLOWABLE	MURA	JUDGE BY LIMIT SAMPLE OR NOT VISIBLE THROUGH ND FILTER	ND 5%
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MURA	JUDGE BY LIMIT SAMPLE OR NOT VISIBLE THROUGH ND FILTER	ND 5%																																										
4	BUBBLES ON POLARIZER /SURFACE STAINS /DIRT/CF FAIL/SPOT	<table border="1"> <thead> <tr> <th></th><th>AVERAGE DIAMETER (mm) : D</th><th>NUMBER OF PIECES PERMITTED</th></tr> </thead> <tbody> <tr> <td rowspan="3">BUBBLE/DENT ON THE POLARIZER</td><td>D ≤ 0.3</td><td>IGNORE</td></tr> <tr> <td>0.3 < D ≤ 0.5</td><td>5</td></tr> <tr> <td>D > 0.5</td><td>0</td></tr> <tr> <td>POLARIZER SCRATCH /FOREIGN LINT</td><td>0.05 < W ≤ 0.2, L ≤ 5,DISTANCE>5</td><td></td></tr> <tr> <td rowspan="3">CF FAIL / SPOT</td><td>D < 0.25, DISTANCE≤10</td><td>IGNORE</td></tr> <tr> <td>0.25 < D ≤ 0.5</td><td>4</td></tr> <tr> <td>0.05 < W ≤ 0.2, L ≤ 5,DISTANCE>5</td><td>3</td></tr> </tbody> </table> NOTE : (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEIOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2 		AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED	BUBBLE/DENT ON THE POLARIZER	D ≤ 0.3	IGNORE	0.3 < D ≤ 0.5	5	D > 0.5	0	POLARIZER SCRATCH /FOREIGN LINT	0.05 < W ≤ 0.2, L ≤ 5,DISTANCE>5		CF FAIL / SPOT	D < 0.25, DISTANCE≤10	IGNORE	0.25 < D ≤ 0.5	4	0.05 < W ≤ 0.2, L ≤ 5,DISTANCE>5	3																						
	AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED																																										
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	0.25 < D ≤ 0.5	4																																										
	0.05 < W ≤ 0.2, L ≤ 5,DISTANCE>5	3																																										

NO.	ITEM	CRITERIA												
5	BLACK/WHITE SPOT CIRCULAR TYPE	THE FOLLOWING BLACK/WHITE SPOT ARE WITHIN THE VIEWING AREA. AVERAGE DIAMETER : D (mm) <table><tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr><tr><td>D≤0.6</td><td>IGNORE</td></tr><tr><td>D>0.6</td><td>0</td></tr></table> NOTE (1) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.		SIZE D	PERMISSIBLE NO.	D≤0.6	IGNORE	D>0.6	0					
SIZE D	PERMISSIBLE NO.													
D≤0.6	IGNORE													
D>0.6	0													
6	SCRATCH	THE FOLLOWING SCRATCH IS WITHIN THE VIEWING AREA. WIDTH : W (mm) , LENGTH : L (mm) <table><tr><th>SIZE W & L</th><th>PERMISSIBLE NO.</th></tr><tr><td>W≤0.3</td><td>IGNORE</td></tr><tr><td>0.3<W≤0.5, L≤5</td><td>9</td></tr><tr><td>W>0.5</td><td>0</td></tr></table> NOTE (1) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.		SIZE W & L	PERMISSIBLE NO.	W≤0.3	IGNORE	0.3<W≤0.5, L≤5	9	W>0.5	0			
SIZE W & L	PERMISSIBLE NO.													
W≤0.3	IGNORE													
0.3<W≤0.5, L≤5	9													
W>0.5	0													
7	BLACK / WHITE LINE LINEAR TYPE / FOREIGN FIBER	THE FOLLOWING BLACK LINE, WHITE LINE IS WITHIN THE VIEWING AREA. WIDTH : W (mm) , LENGTH : L (mm) <table><tr><th>SIZE W & L</th><th>PERMISSIBLE NO.</th></tr><tr><td>W≤0.3</td><td>IGNORE</td></tr><tr><td>0.3<W≤0.5, L≤5</td><td>7</td></tr><tr><td>W>0.5</td><td>0</td></tr></table> NOTE (1) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.		SIZE W & L	PERMISSIBLE NO.	W≤0.3	IGNORE	0.3<W≤0.5, L≤5	7	W>0.5	0			
SIZE W & L	PERMISSIBLE NO.													
W≤0.3	IGNORE													
0.3<W≤0.5, L≤5	7													
W>0.5	0													
8	BUBBLE / DENT FOR OPTICAL BONDING	BUBBLES WITHIN VIEWING AREA. AVERAGE DIAMETER : D (mm) <table><tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr><tr><td>D≤0.2</td><td>IGNORE</td></tr><tr><td>0.2<D≤0.3</td><td>3</td></tr><tr><td>0.3<D≤0.5</td><td>4</td></tr><tr><td>D>0.5</td><td>0</td></tr></table> NOTE (1) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.		SIZE D	PERMISSIBLE NO.	D≤0.2	IGNORE	0.2<D≤0.3	3	0.3<D≤0.5	4	D>0.5	0	
SIZE D	PERMISSIBLE NO.													
D≤0.2	IGNORE													
0.2<D≤0.3	3													
0.3<D≤0.5	4													
D>0.5	0													
9	CHIPPING	<table><tr><td>CORNER</td><td>X ≤ 3mm 、 Y ≤ 3mm 、 Z ≤ t (t : THICKNESS)</td></tr><tr><td>EDGE</td><td>X ≤ 6mm , Y ≤ 1mm , Z < t (t : THICKNESS)</td></tr></table>	CORNER	X ≤ 3mm 、 Y ≤ 3mm 、 Z ≤ t (t : THICKNESS)	EDGE	X ≤ 6mm , Y ≤ 1mm , Z < t (t : THICKNESS)								
CORNER	X ≤ 3mm 、 Y ≤ 3mm 、 Z ≤ t (t : THICKNESS)													
EDGE	X ≤ 6mm , Y ≤ 1mm , Z < t (t : THICKNESS)													
10	CRACKED GLASS	NOT ACCEPTABLE												
11	PIN HOLE / IMPURITIES ON PRINTING	AVERAGE DIAMETER : D (mm) <table><tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr><tr><td>D≤0.1</td><td>IGNORE</td></tr><tr><td>0.1<D≤0.2</td><td>5</td></tr><tr><td>0.2<D≤0.3</td><td>1</td></tr><tr><td>D>0.3</td><td>0</td></tr></table> NOTE (1) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART. NOTE (2) : REFILL INK IS ACCEPTABLE, BUT LOGO AND ICON PIN HOLE ARE NOT ALLOWED.		SIZE D	PERMISSIBLE NO.	D≤0.1	IGNORE	0.1<D≤0.2	5	0.2<D≤0.3	1	D>0.3	0	
SIZE D	PERMISSIBLE NO.													
D≤0.1	IGNORE													
0.1<D≤0.2	5													
0.2<D≤0.3	1													
D>0.3	0													
12	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED.												
13	MURA ON DISPLAY	IT'S ACCEPTABLE, IF MURA IS SLIGHT VISIBLE THROUGH 5% ND FILTER.												
14	UNEVEN COLOR SPREAD, COLORATION	TO BE DETERMINED BASED UPON THE LIMITED SAMPLE.												
15	BEZEL APPEARANCE	1. BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. 2. BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.												

NO.	ITEM	CRITERIA
16	PCB	1. THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES. 2. NO OXIDATION OR CONTAMINATION ON PCB TERMINALS. 3. PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS. 4. THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART. 5. IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.
17	SOLDERING	1. NO SOLDERING FOUND ON THE SPECIFIED PLACE 2. INSUFFICIENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD  (b)CHIP COMPONENT · SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING  · SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED  3. PARTS ALIGNMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE  (b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE  4. NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. 5. NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. 6. NO RESIDUE OR SOLDER BALLS ON PCB. 7. NO SHORT CIRCUITS IN COMPONENTS ON PCB.

NO.	ITEM	CRITERIA
18	BACKLIGHT	1. NO LIGHT 2. FLICKERING AND OTHER ABNORMAL ILLUMINATION 3. SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. 4. BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
19	GENERAL APPEARANCE	1. NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. 2. NO CRACKS ON INTERFACE PIN (OLB) OF TCP. 3. NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. 4. THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. 5. THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. 6. THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. 7. SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. 8. PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. 9. LCD PIN LOOSE OR MISSING PINS. 10. PRODUCT PACKAGING MUST BE THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. 11. PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. 12. THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

14 RELIABILITY TEST

14.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT (+70°C) FOR 240 HRS
2	LOW TEMPERATURE TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT (-20°C) FOR 240 HRS
3	HIGH TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT (+80°C) FOR 240 HRS
4	LOW TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT (-30°C) FOR 240 HRS
5	HIGH TEMPERATURE / HUMIDITY TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK TEST (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION :
7	VIBRATION (NOT OPERATED)	11.76 m/s ² (1.2G), 10~100 Hz XYZ DIRECTIONS, 1 HR EACH
8	SHOCK (NOT OPERATED)	490.0 m/s ² (50G), 10 ms XYZ DIRECTIONS, 1 TIME EACH
9	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV (ACCORDING TO IEC-61000-4-2)

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

NOTE (2) : WHEN THE LCD MODULE IS OPERATED AT A HIGHER AMBIENT TEMPERATURE THAN 60°C, THE PWM DUTY CYCLE OF THE LED BACKLIGHT SHOULD BE ADJUSTED TO BE LESS THAN TBD%. IF THE MODULE IS OPERATED AT A HIGHER DUTY CYCLE THAN TBD, THEN THERE IS A POSSIBILITY OF DISTORTION AND IRREGULARITY OF THE PICTURE DUE TO LIQUID CRYSTAL BEHAVIOR.

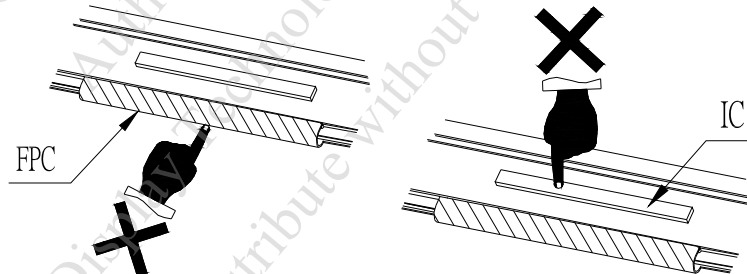
NOTE (3) : TESTING CONDITIONS AND INSPECTION CRITERIA

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

15. CAUTION

15.1 OPERATION

- 15.1.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 15.1.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR.
WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .
- 15.1.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.
- 15.1.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE .
IF ABOVE SEQUENCE IS NOT FOLLOWED , CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM .
- 15.1.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!
DO NOT STRESS FPC AND IC ON THE MODULE!



15.2 STORAGE

- 15.2.1 STORE THE MODULE IN A DARK ROOM OR KEEP IN ORIGINAL PACKAGE WHERE MUST KEEP AT $25 \pm 10^{\circ}\text{C}$ AND 65%RH OR LESS.
- 15.2.2 DO NOT STORE THE MODULE IN SURROUNDINGS CONTAINING ORGANIC SOLVENT OR CORROSIVE GAS.
- 15.2.3 STORE THE MODULE IN AN ANTI-ELECTROSTATIC CONTAINER OR BAG.

15.3 NOTICE

- 15.3.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 15.3.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 15.3.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 15.3.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .
- 15.3.5 DON'T GIVE EXTERNAL SHOCK.
- 15.3.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 15.3.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC.
WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 15.3.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 15.3.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS AND SOLVENT.
- 15.3.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 15.3.11 REWIRING: NO MORE THAN 3 TIMES.