



上海三木电子有限公司

San Technology (Shanghai) Co., Ltd.

SPECIFICATION FOR LCD MODULE

Customer P/N:

Santek P/N: ST056014WCY-RSCLW-F

DOC. Revision: RS01

Customer Approval:

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	SIGNATURE	DATE
PREPARED BY	Jacky Zhou	2013-09-05
CHECKED BY	Natty Lee	2013-Sep-9
APPROVED BY	<i>Y. Shiroki</i>	<i>2013-09-09</i>

Document Revision History

Revision	Date	Description	Changed by
RS01	2013-09-05	Initial Release	Jacky Zhou

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1. General Specifications

No.	Item	Specification	Remark
1	LCD size	5.6 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	640 × (RGB) × 480	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.0588(W) × 0.1764(H) mm	
6	Active area	112.896 (W) × 84.672(H) mm	
7	Module size	153.9(W) × 125.67(H) × 9.8(D)	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight Power consumption	3.04W(Typ.)	Note 2
12	Panel Power consumption	0.66 W(Typ.)	Note 3
13	Weight	219g	

Note 1: Refer to Mechanical Drawing.

Note 2: Including LED Driver power consumption.

Note 3: Including T-con Board power consumption.

X axis linear resistance	≤ 15kΩ	
X axis linear resistance	≤ 15kΩ	
X ~ X axis and Y ~ Y axis between resistance	≥ 20MΩ	
X ~ Y axis between resistance	≥ 20MΩ	
Chattering	16 msec or less	
IC NO	Focaltech FT5316	

2.5 Mechanical characteristics

Item	Performance	Remark
Input method	Use of an exclusive pen or finger	
Hardness Of Surface	Surface : >5H [JIS K 5400]	Hard
Accuracy	+/-1mm@10mm	mm
Support Operation	Finger 5	
Channel	20*12	
Resolution	480*640	
Interface	I2C	
Supported Operating Systems	Android2.0-2.3,Window7	

2.6 Optical characteristics

Item	Performance	Remark
Total light transmittance	≥ 85%	
Haze	≤ 3%	

2.7. CTP PIN Assignment

PIN	Signal	Description
1	GND	Ground
2	RESET	
3	SDA(3.3)	I2C Serial Data Input And Output.
4	SCL(3.3)	I2C Serial Clock Input.
5	INT	An interrupt signal to inform the host processor that touch data is ready for read
6	VDD	Supply Voltage

3.LCM Pin Assignment

TFT LCD Panel Driving Section

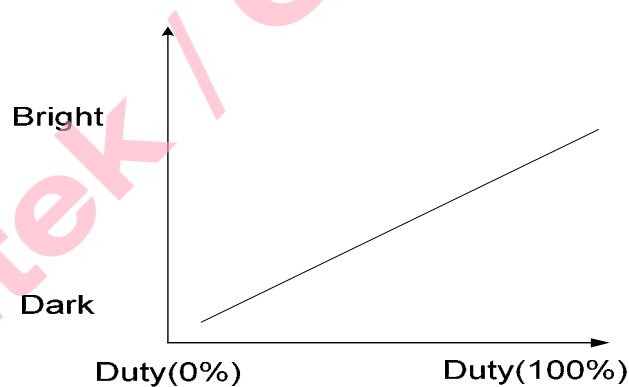
FPC connector is used for the module electronics interface. The recommended model is FH19-40S -0.5SH manufactured by HiRose.

Pin No.	Symbol	I/O	Function	Remark
1	NC			
2	NC			
3	NC			
4	NC			
5	NC			
6	V _{CC}	P	Power Voltage for digital circuit	
7	V _{CC}	P	Power Voltage for digital circuit	
8	MODE	I	DE or HV mode control	Note 3
9	DE	I	Data enable	
10	VS	I	Vsync signal input	
11	HS	I	Hsync signal input	
12	GND	P	Power ground	
13	B5	I	Blue data input (MSB)	
14	B4	I	Blue data input	
15	B3	I	Blue data input	
16	GND	P	Power ground	
17	B2	I	Blue data input	
18	B1	I	Blue data input	
19	B0	I	Blue data input(LSB)	
20	GND	P	Power ground	
21	G5	I	Green data input(MSB)	
22	G4	I	Green data input	
23	G3	I	Green data input	
24	GND	P	Power ground	
25	G2	I	Green data input	

26	G1	I	Green data input	
27	G0	I	Green data input(LSB)	
28	GND	P	Power ground	
29	R5	I	Red data input(MSB)	
30	R4	I	Red data input	
31	R3	I	Red data input	
32	GND	P	Power ground	
33	R2	I	Red data input	
34	R1	I	Red data input	
35	R0	I	Red data input(LSB)	
36	GND	P	Power ground	
37	DCLK	I	Sample clock	
38	GND	P	Power ground	
39	L/R	I	Select left to right scanning direction	Note4,5
40	U/D	I	Select up or down scanning direction	Note4,5

Note: I: input, O: output, P: Power

Note1: Pin.3 is used to adjust brightness.



Note 2: ADJ signal=0~3.3V, operation frequency: 100~300Hz

F=100~300Hz

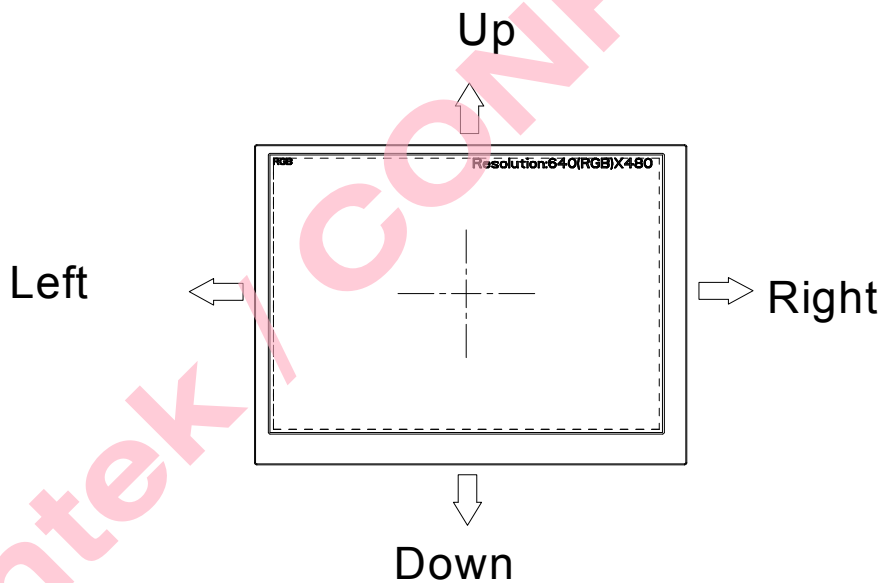


Note 3: DE Mode, Mode="H", HS floating and VS floating
HV Mode, Mode="L" and DE floating

Note 4: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	V _{CC}	Up to down, left to right
V _{CC}	GND	Down to up, right to left
GND	GND	Up to down, right to left
V _{CC}	V _{CC}	Down to up, left to right

Note 5: Definition of scanning direction.
Refer to the figure as below:



4. Operation Specifications

4.1. Absolute Maximum Ratings

(Note 1)

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	V_{CC}	-0.3	6.5	V	
	V_{LED}	-0.3	6.5	V	
Operation temperature	T_{OP}	-20	70	°C	
Storage temperature	T_{ST}	-30	80	°C	

Note 1: The absolute maximum rating values of the module should not be exceeded. Once exceeded absolute maximum rating values, the characteristics of the module may not be recovered. Even in an extreme condition, may result in module permanently destroyed.

4.2. Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V _{CC}	3.1	3.3	3.5	V	Note 1
	V _{LED}	8.0	15.2	16.0	V	Note 2
Current Consumption	I _{CC}	-	200	250	mA	
	I _{LED}	-	200	-	mA	Note 3
Input logic high voltage	V _{IH}	0.7V _{CC}	-	1V _{CC}	V	Note 4
Input logic low voltage	V _{IL}	0	-	0.3V _{CC}	V	
LED life time	-	20,000	-	-	Hr	Note 5

Note 1: V_{CC} setting should match the signals output voltage (refer to Note 4) of customer's system board.

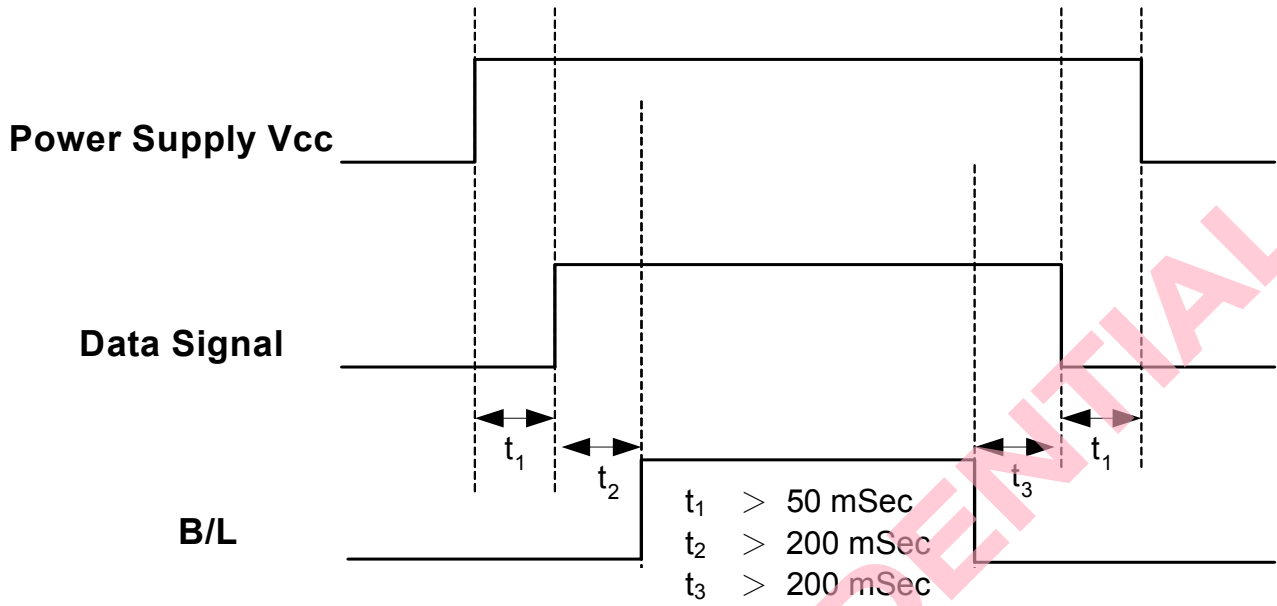
Note 2: LED driving voltage.

Note 3: LED driving current.

Note 4: DCLK, DE, HS, VS, R0~ R5,, G0~ G5, B0~ B5.

Note 5: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I_{LED}=200mA. The LED lifetime could be decreased if operating I_{LED} is larger than 200mA.

4.3. Power Sequence



Note: Data includes DE, VS, HS, B0~B5, G0~G5, R0~R5, DCLK.

4.4. Timing Characteristics

4.4.1. Timing Conditions

Input/Output Timing

Item	Symbol	Values			Unit.	Remark
		Min.	Typ.	Max.		
PXLCLK clock time	Tclk	33.3	39.7	-	ns	
PXLCLK pulse duty	Tcwh	40	50	60	%	Tclk
DATA set-up time	Tdsu	12	-	-	ns	DATA to PXLCLK
DATA hold time	Tdhd	12	-	-	ns	DATA to PXLCLK
DE setup time	Tesu	12	-	-	ns	DE to PXLCLK
VSYNC setup time	Tvst	12	-	-	ns	
VSYNC hold time	Tvhd	12	-	-	ns	
HSYNC setup time	Thst	12	-	-	ns	
HSYNC hold time	Thhd	12	-	-	ns	
HSYNC period time	Th	22.91	31.76	-	us	
HSYNC width	Thwh	1	-	-	Tclk	
VSYNC width	Tvwh	1	-	-	Th	
HSYNC to CLKIN	Thc	-	-	1	Tclk	

DE Mode input Timing Limitation

DE Mode	Values			Unit	Remark
	Min.	Typ.	Max.		
THC	48	160	765	tclk	
THD	640	640	640	tclk	
TH	688	800	1405	tclk	1TH=1line
TVC	6	45	255	line	
TVD	480	480	480	line	
TV	486	525	735	line	1TV=1field

HV Mode input Timing Limitation

HV Mode	Values			Unit	Remark
	Min.	Typ.	Max.		
Thwh	-	10	-	tclk	
Thbp	-	134	-	tclk	
Thfp	-	16	-	tclk	
THD	-	640	-	tclk	
TH	-	800	-	tclk	1TH=1 line
Tvwh	-	2	-	line	
Tvbp	-	11	-	line	
Tvfp	-	32	-	line	
TVD	-	480	-	line	
TV	-	525	-	line	1TV=1 field

4.4.2. Timing Diagram

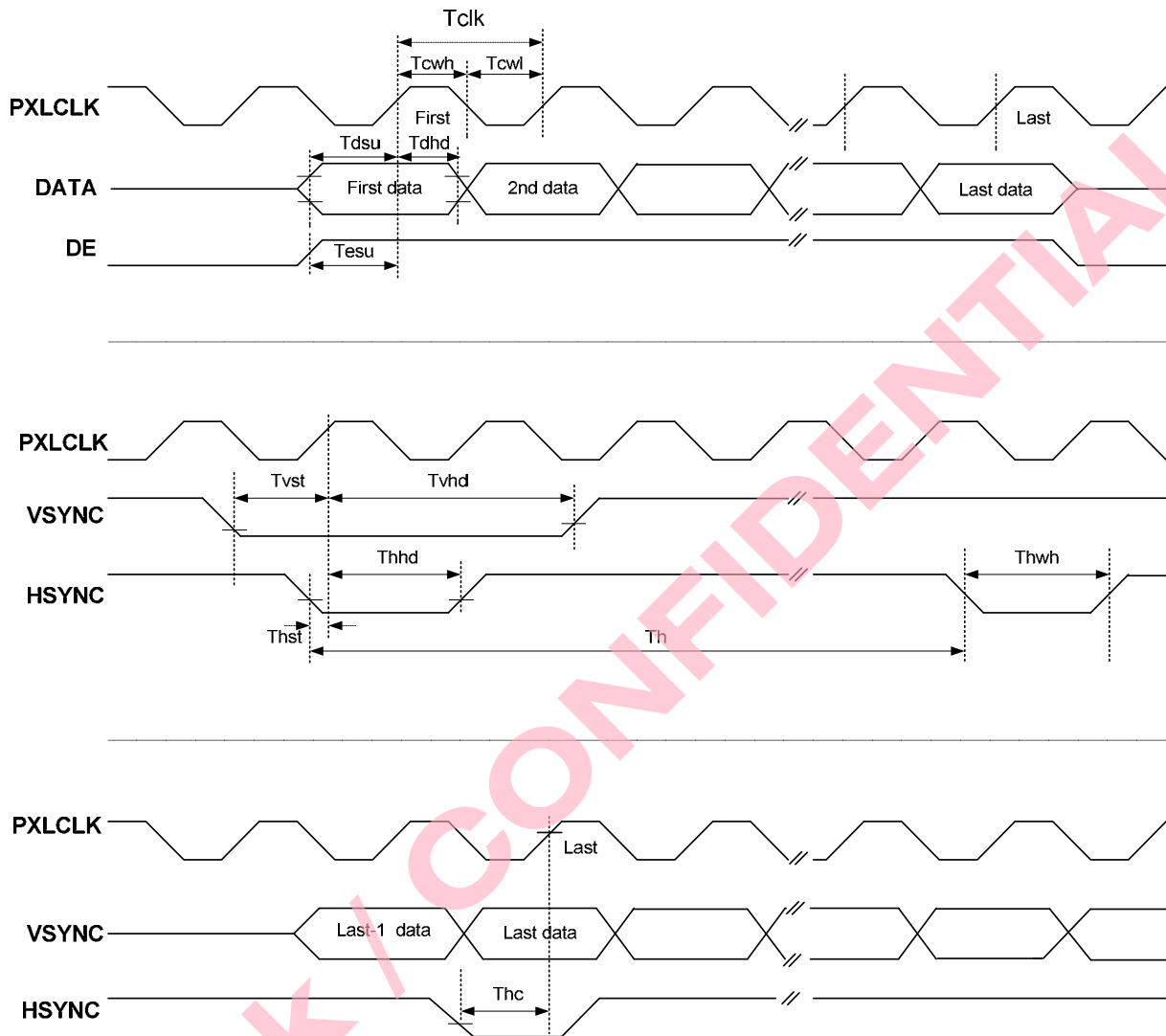


Fig.3-1 Clock and Data Input Timing Diagram

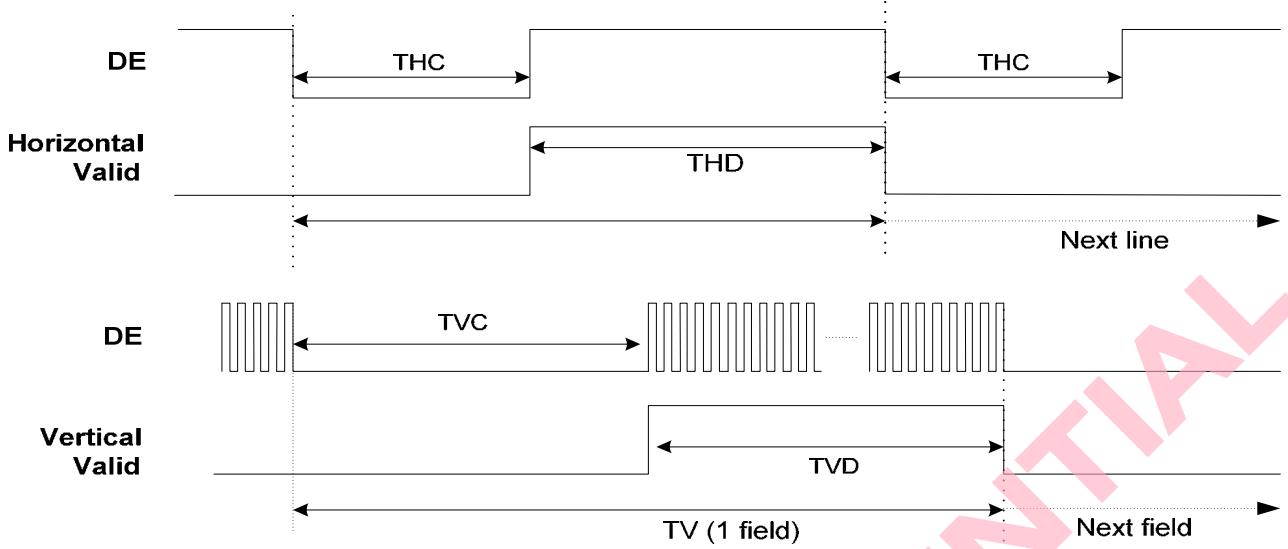


Fig.3-2 DE Mode Input Timing

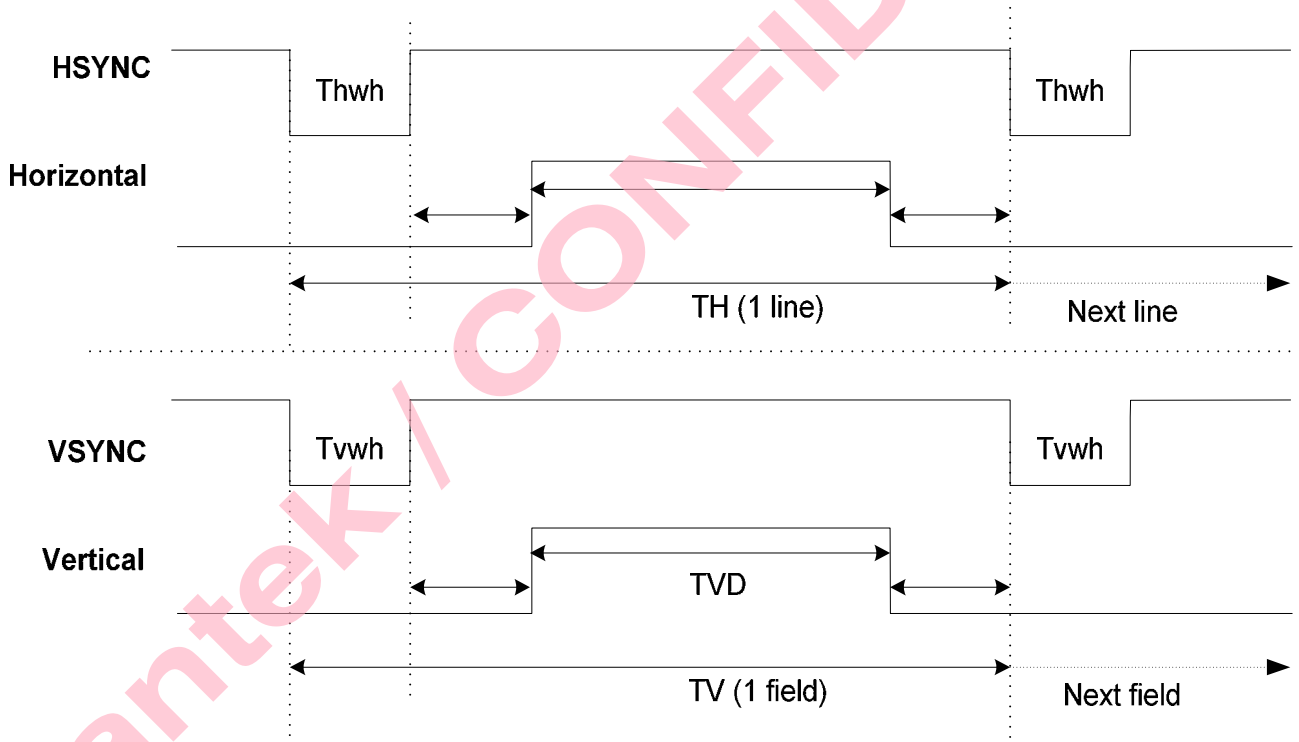


Fig.3-3 HV Mode Input Timing

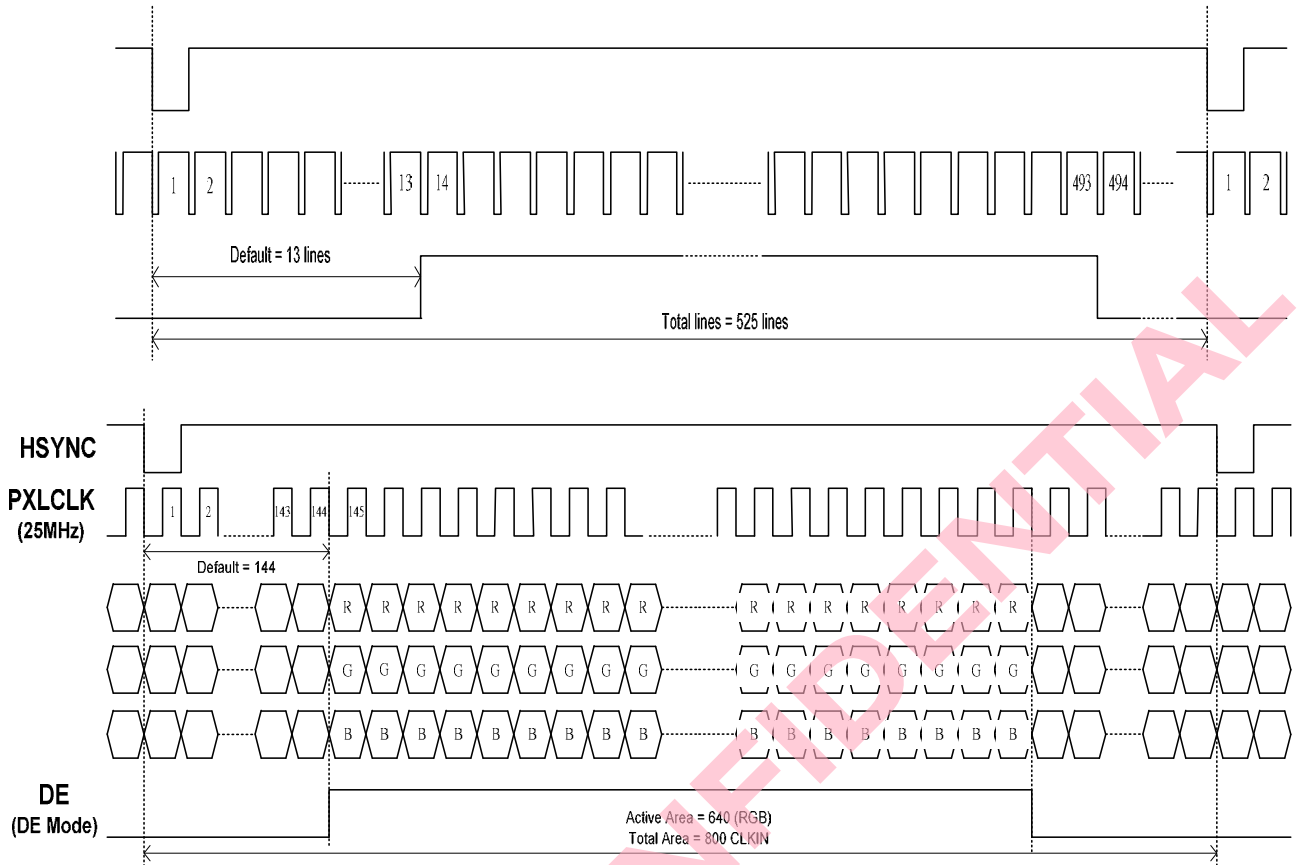


Fig. 3-4 18 bit RGB mode for 640 x (RGB) x 480

5. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥10)	θ_L	$\Phi=180^\circ$ (9 o'clock)	60	70	-	degree	Note 1
	θ_R	$\Phi=0^\circ$ (3 o'clock)	60	70	-		
	θ_T	$\Phi=90^\circ$ (12 o'clock)	40	50	-		
	θ_B	$\Phi=270^\circ$ (6 o'clock)	60	70	-		
Response time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5 Note 6
	W_Y		0.28	0.33	0.38	-	
Luminance	L_1		896	1120	-	cd/m ²	Note 6
Luminance uniformity	Y_U	70	75	-	%	Note 6,7	

Test Conditions:

1. $V_{CC}=3.3V$, $I_{LED}=200mA$, the ambient temperature is 25 .
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

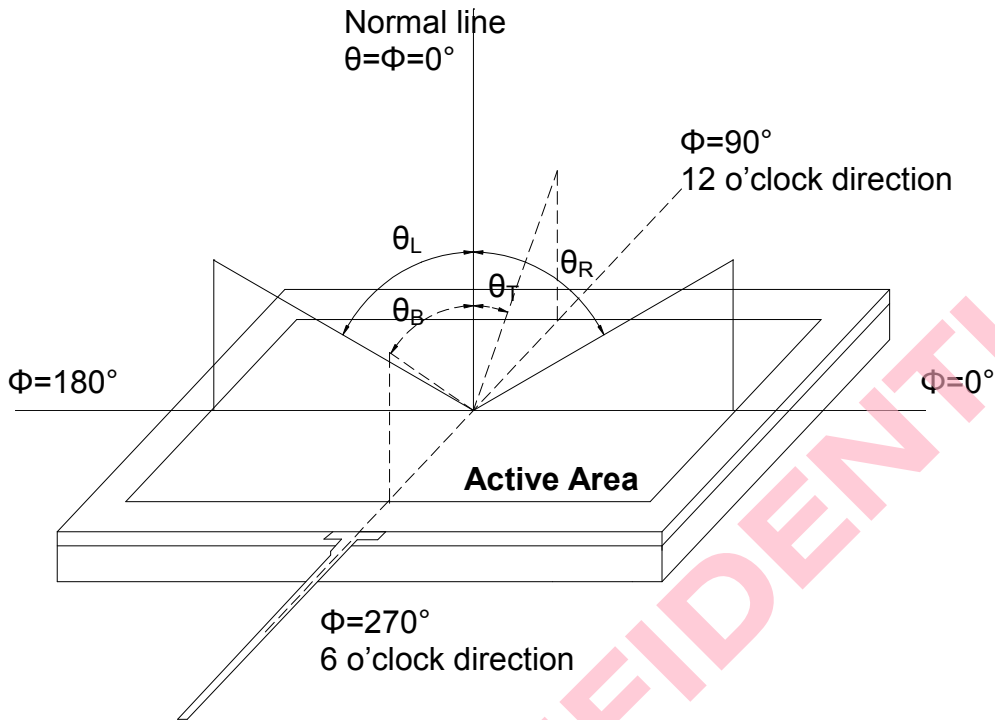


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

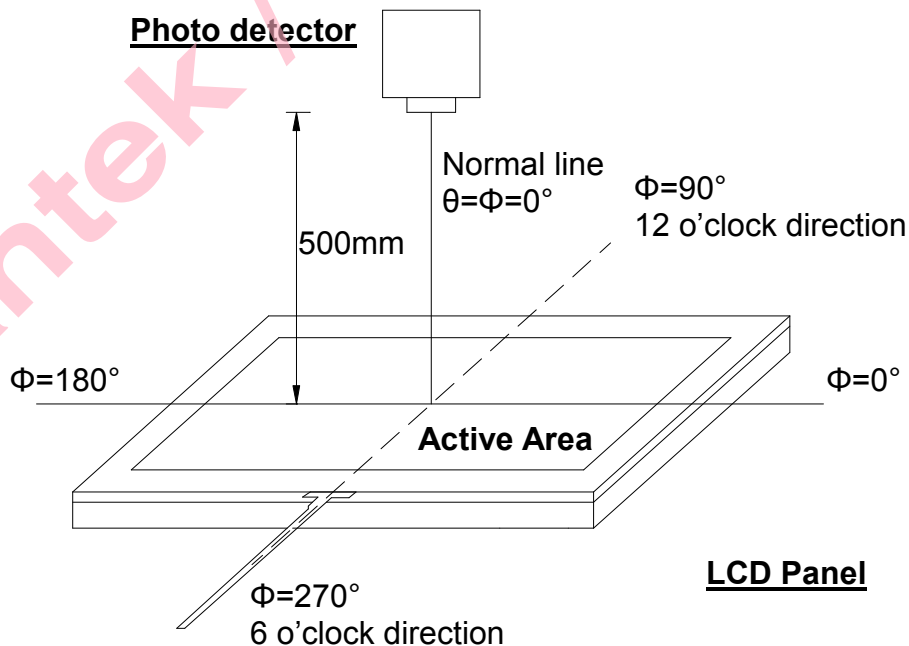


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

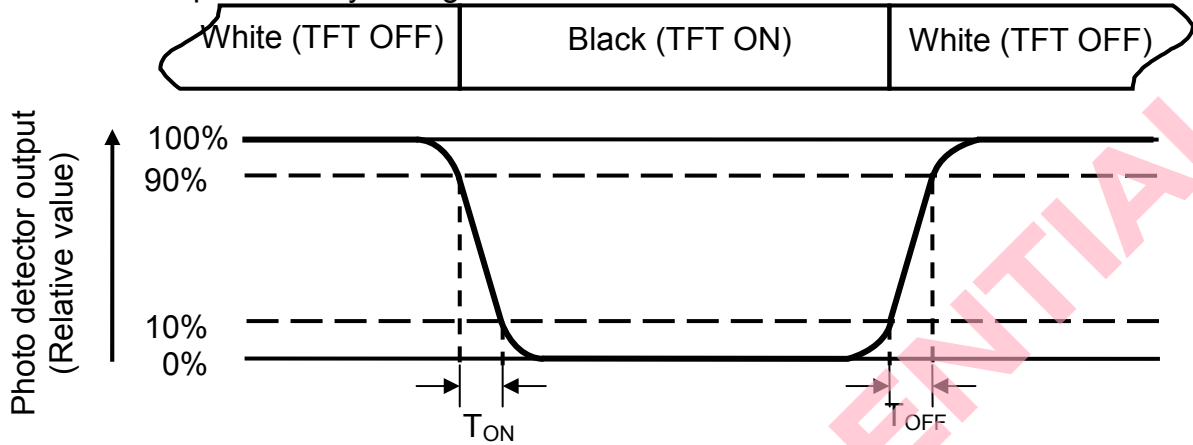


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $I_{LED}=200\text{mA}$

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

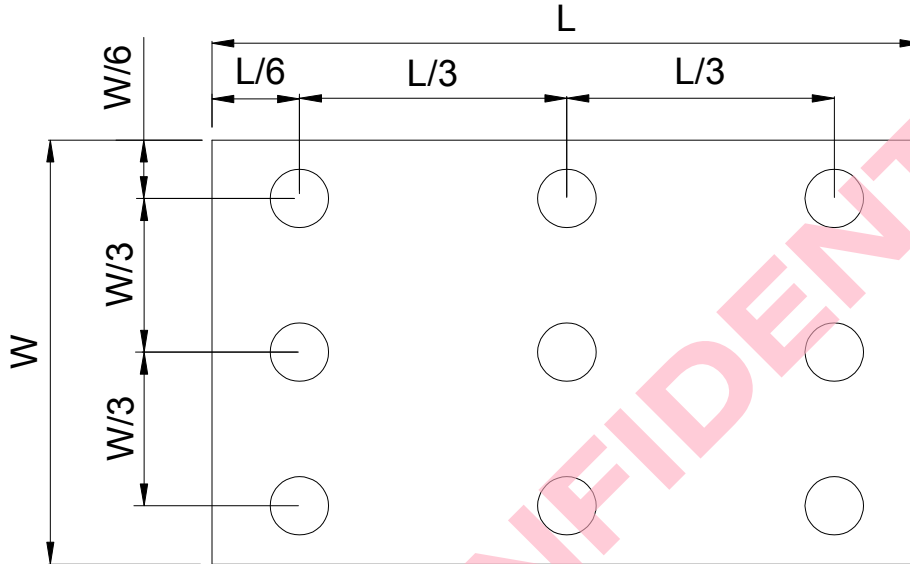


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.

7. General Precautions

7.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

7.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

7.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

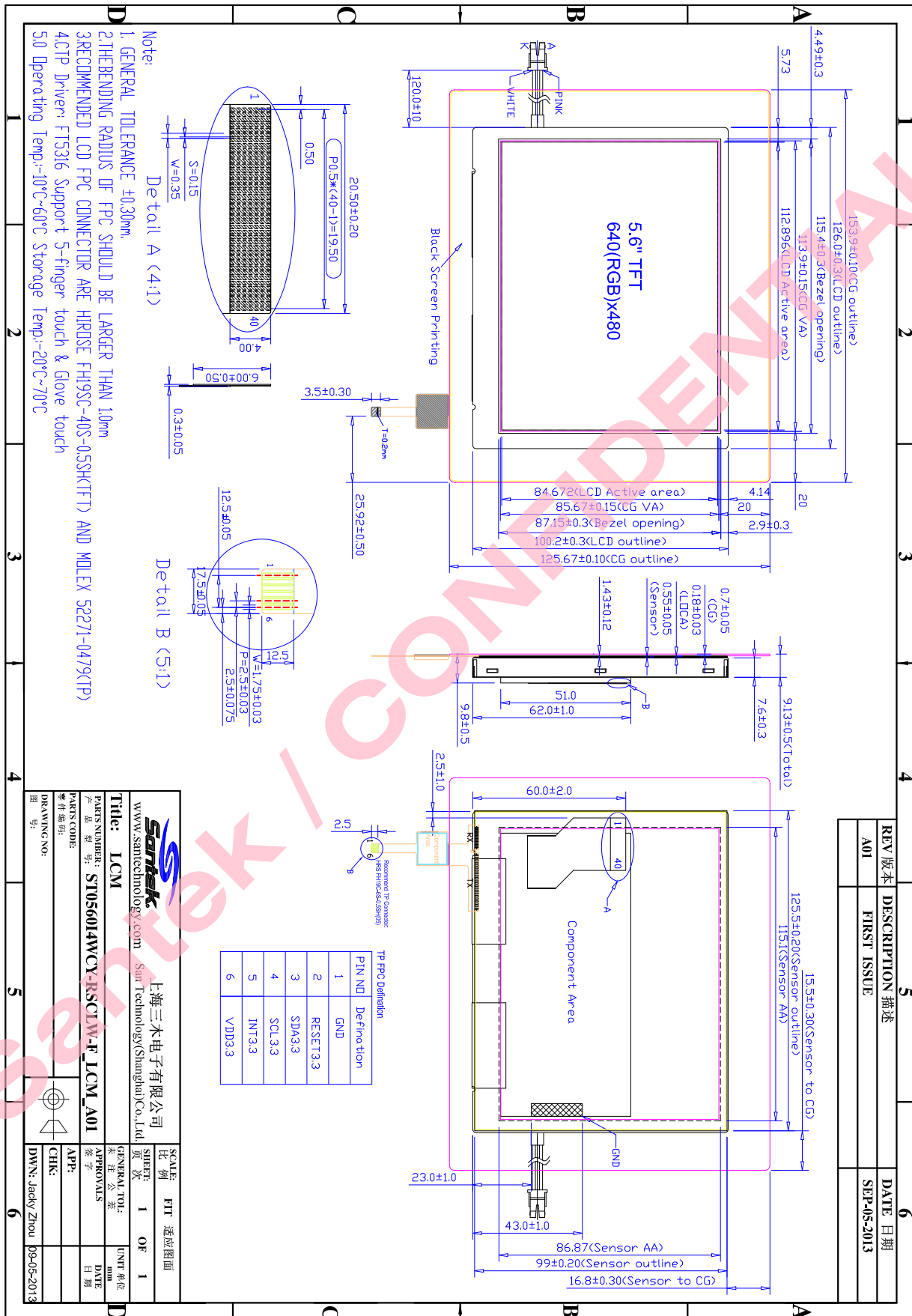
7.4. Storage

1. Store the module in a dark room where must keep at $+25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

7.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft cloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

8. Mechanical Drawing



REV 版本	DESCRIPTION 描述	DATE 日期
A01	FIRST ISSUE	SEP-05-2013

Title: LCM		SCALE: FIT 适应图面	
PARTS NUMBER: ST056014WCY-RSCLW-F LCM A01		SHEET: 1 OF 1	
DRAWING NO:		DATE:	
CHK:		APP:	
DWY: Jacky Zhou		DESIGN: 2013	

9. Packaging Drawing (T.B.D)

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